

FIG. 1

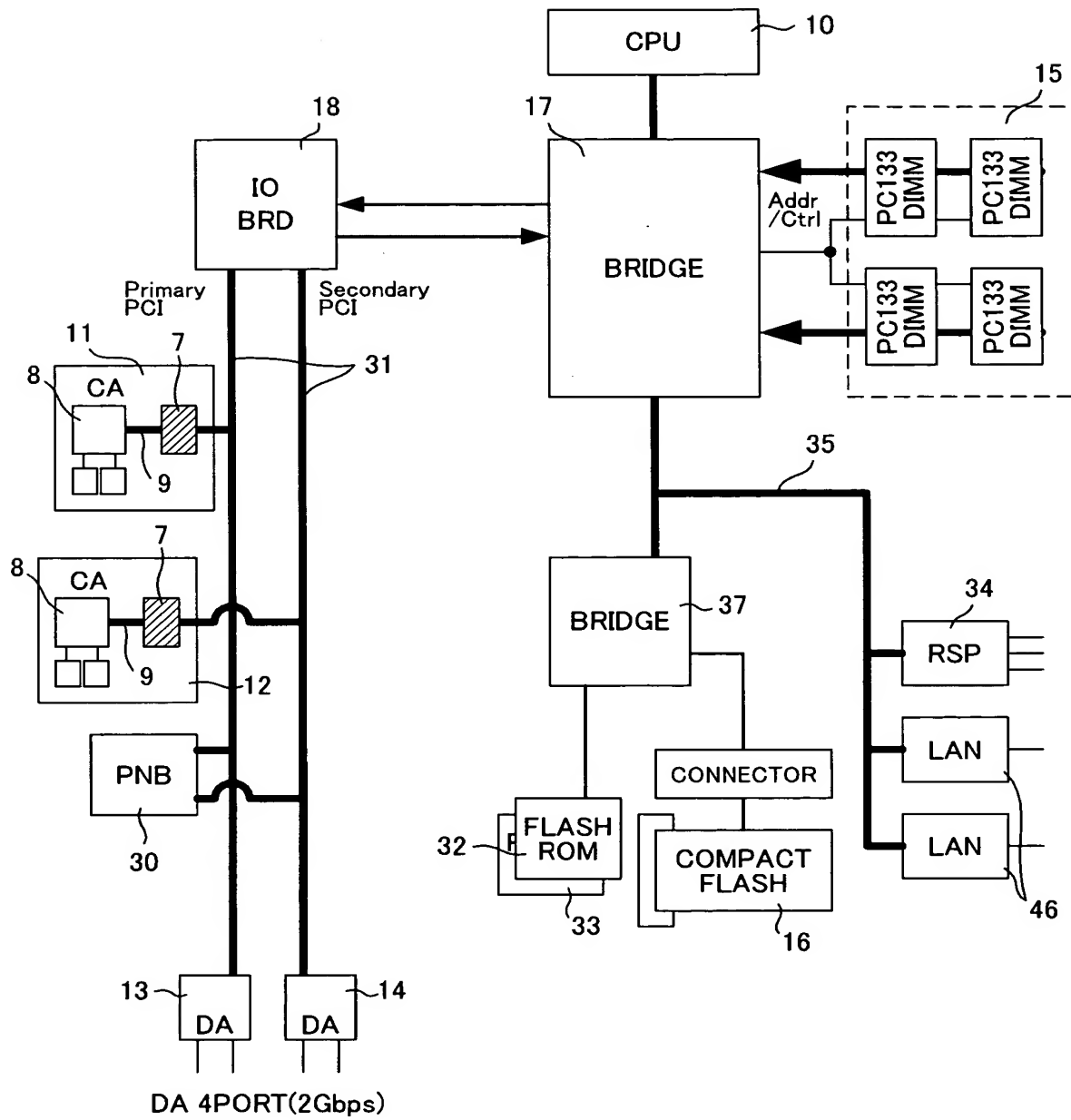


FIG. 2

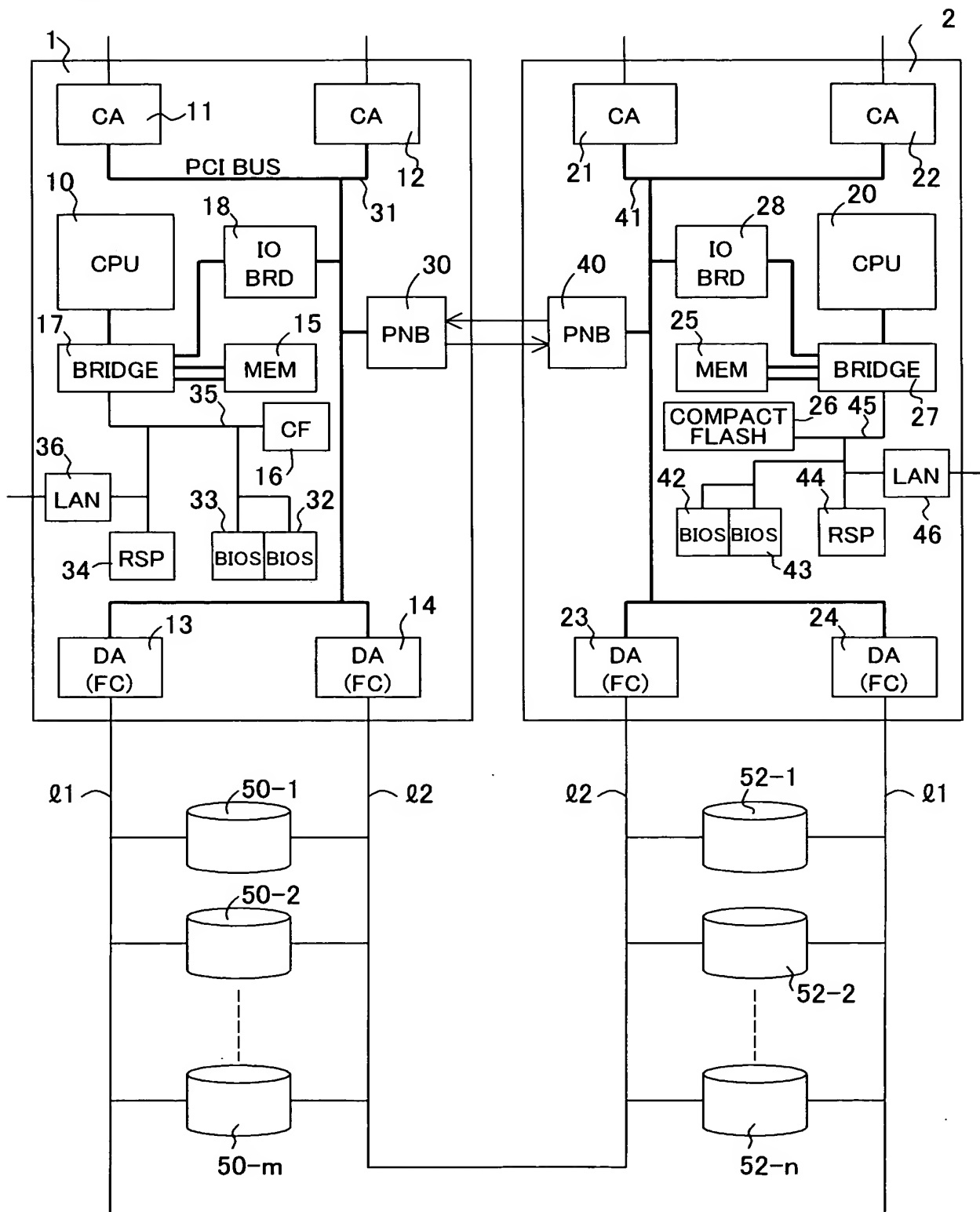


FIG. 3

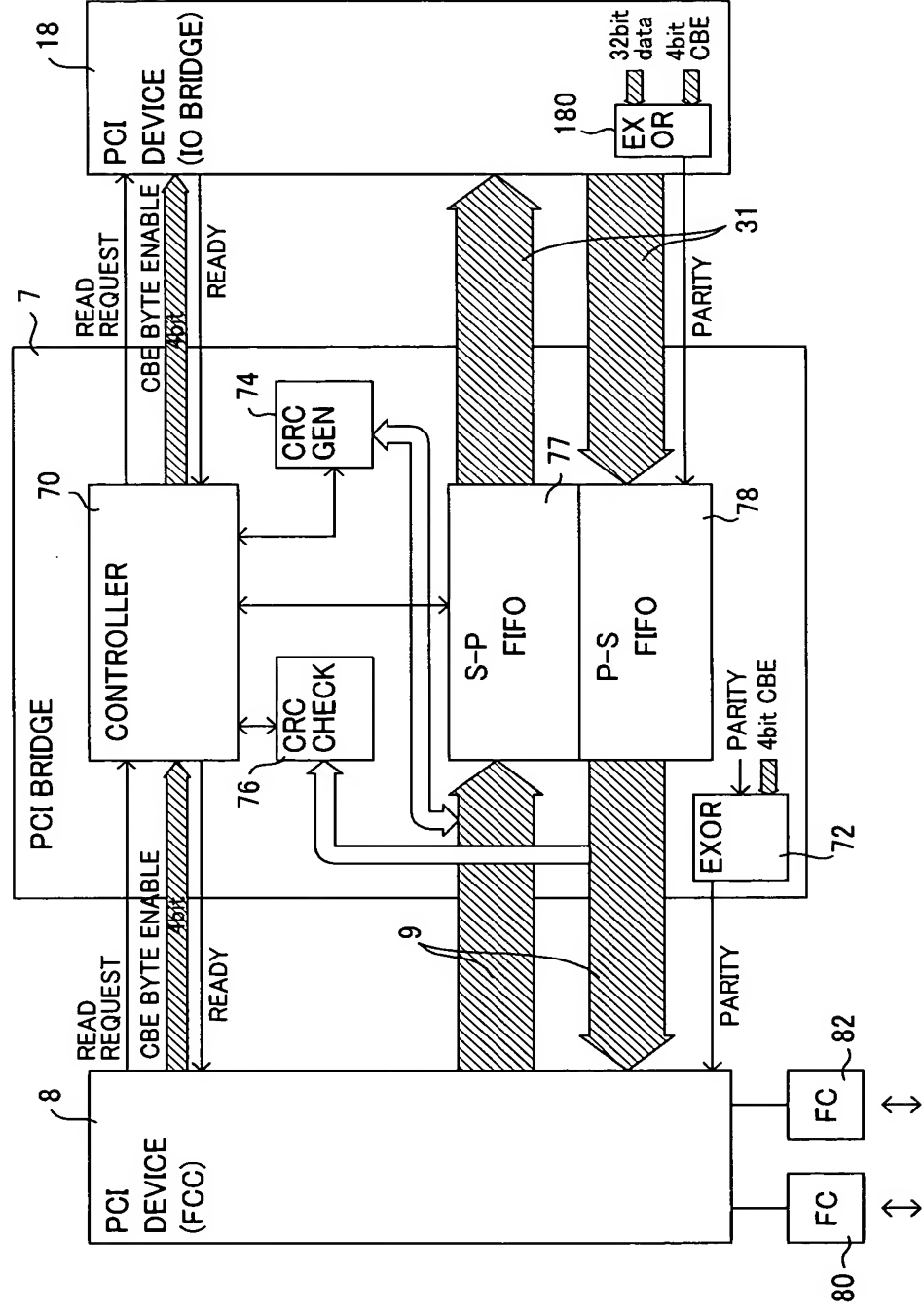


FIG. 4

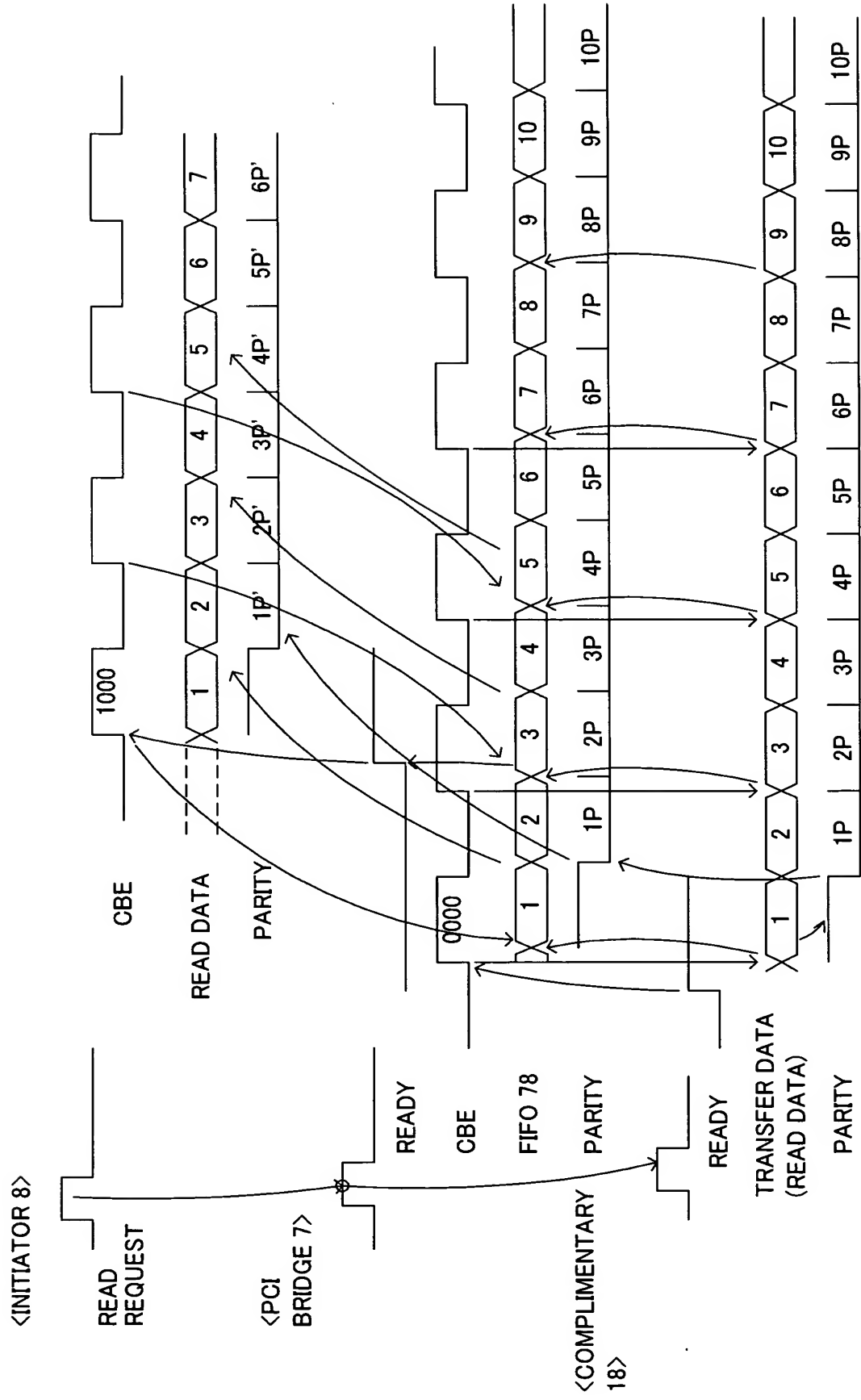


FIG. 5

NORMAL

	INITIATOR-SITE BUS	COMPLEMENTARY-SIDE BUS	NOTES
READ DATA (AD)	0x0000_0000 (32bits)	0x0000_0000 (32bits)	TRANSFER DIRECTLY FROM COMPLEMENTARY TO INITIAOR
BYTE-ENABLE (CBE)	1000 (4bits) (A)	0000 (4bits)	VALUE OF COMPLEMENTALY SIDE IS ALWAYS ALL "0", VALUE OF INITIATOR SIDE IS NOT PREDICTED
PARITY (PAR)	1 (1bits) (C)	0 (1bits) (B)	CORRECTLY CREATE BY XOR OF PARITY (C), BYTE ENABLE (A) AND PARITY (B)

PARITY ERROR

	INITIATOR-SITE BUS	COMPLEMENTARY-SIDE BUS	NOTES
READ DATA (AD)	0x0000_1000 (32bits)	0x0000_1000 (32bits)	TRANSFER DIRECTLY FROM COMPLEMENTARY TO INITIAOR
BYTE-ENABLE (CBE)	1000 (4bits) (A)	0000 (4bits)	VALUE OF COMPLEMENTALY SIDE IS ALWAYS ALL "0", VALUE OF INITIATOR SIDE IS NOT PREDICTED
PARITY (PAR)	1 (1bits) (C)	0 (1bits) (B)	PARITY OF COMPLEMENTARY SIDE IS ERROR VALUE, ERROR PARITY IS TRANSMITTED BY XOR OF PARITY (C), BYTE ENABLE (A) AND PARITY (B)

FIG. 6

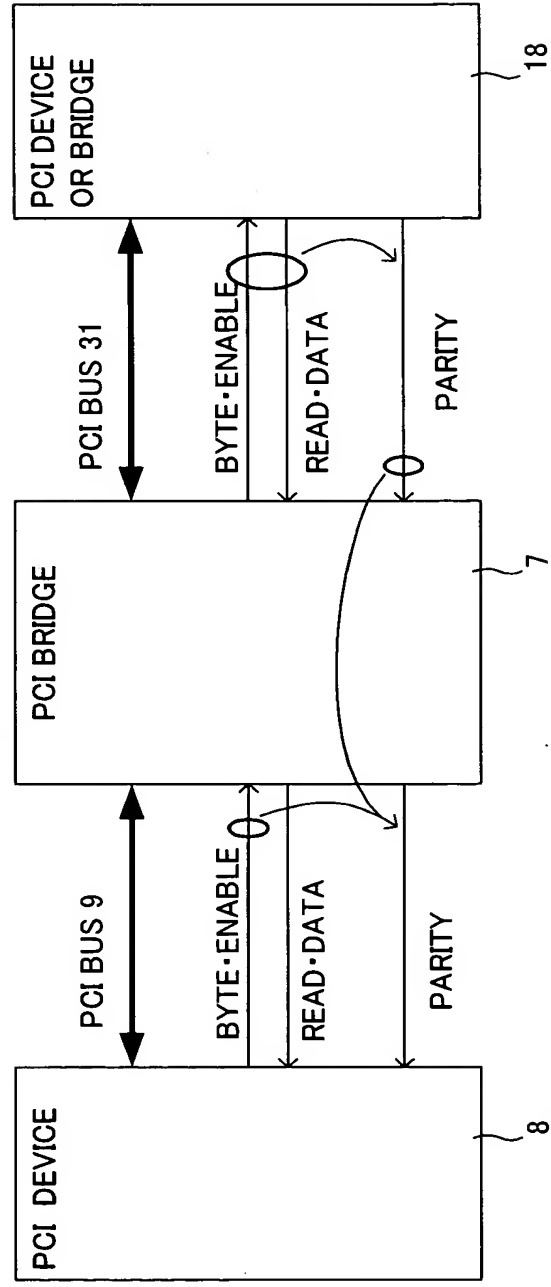


FIG. 7

PRIOR ART

